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**INFORMATION DISCLOSURE CITATION  
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Appleton, Henry

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B.H. Lee et al.

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U. S. PATENT DOCUMENTS

## FOREIGN PATENT DOCUMENTS

**OTHER DOCUMENTS** (including Author, Title, Date, Printed Pages, Etc.)

*pp* S. Wolf et al., "Silicon Processing for the VLSI Era, Volume 2: Process Integration", Lattice Press, Sunset Beach, California, c. 1990, pp. 332-334.

DINNER

ER  
J. R. Hall

**DATE CONSIDERED**

10/30/01

**EXAMINER:** Initial if citation considered, whether or not citation is in conformance with MPEP § 609; Draw line through